

## WEST Search History





DATE: Sunday, August 05, 2007

Hide?	Set Name	Query	Hit Count
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L23	L22 and L21 and L3	161
<input type="checkbox"/>	L22	((serializ\$3 near2 deserializ\$3) or serdes\$2) near7 (plural\$4 or multipl\$4 or several\$3 or numerous\$2)	452
<input type="checkbox"/>	L21	709/\$5.ccls. or 710/\$5.ccls. or 713/\$5.ccls. or 370/\$5.ccls. or 375/\$5.ccls.	270282
<input type="checkbox"/>	L20	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) near7 (plural\$4 or multipl\$4)	30
<input type="checkbox"/>	L19	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) and (disc near3 drive) and L3	8
<input type="checkbox"/>	L18	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) and (substrate\$ and board) and (disc near3 drive) and L3	1
<input type="checkbox"/>	L17	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) and (substrate\$ and board)	57
<input type="checkbox"/>	L16	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) and (lane and substrate\$ and board)	3
<input type="checkbox"/>	L15	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) same (lane and substrate\$ and board)	2
<input type="checkbox"/>	L14	20040120418	2
<input type="checkbox"/>	L13	L12 and L10 and L3	0
<input type="checkbox"/>	L12	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes\$2))	575
<input type="checkbox"/>	L11	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes\$2)) and (disc\$ near3 drive)	8
<input type="checkbox"/>	L10	(375/211).ccls. or (455/7).ccls. or (455/14).ccls. or (455/15).ccls. or (455/16).ccls. or (370/279).ccls. or (370/293).ccls. or (379/338).ccls.	2895
<input type="checkbox"/>	L9	20040179638	2
<input type="checkbox"/>	L8	L7 and L3	31
<input type="checkbox"/>	L7	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes\$2)) same (transceiver\$)	55
<input type="checkbox"/>	L6	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes\$2)) same (transceiver\$) and (disc near3 drive)	7
<input type="checkbox"/>	L5	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes\$2)) same (transceiver\$) same (disc near3 drive)	2
<input type="checkbox"/>	L4	L3 and L2	59
<input type="checkbox"/>	L3	@ad<=20040130	25461944

<input type="checkbox"/>	L2	((serializ\$3 near2 deserializ\$3) or serdes) same (disc near3 drive)	84
<input type="checkbox"/>	L1	((serializ\$3 near2 deserializ\$3) or serdes) same (phase near3 lack\$3) same (disc near3 drive)	0

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<input type="checkbox"/>	L19	((serializ\$3 near2 deserializ\$3) or serdes\$2) near6 core\$) and (disc near3 drive) and l3	8
<input type="checkbox"/>	L18	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) and (substrate\$ and board) and (disc near3 drive) and l3	1
<input type="checkbox"/>	L17	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) and (substrate\$ and board)	57
<input type="checkbox"/>	L16	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) and (lane and substrate\$ and board)	3
<input type="checkbox"/>	L15	((serializ\$3 near2 deserializ\$3) or serdes) near6 core\$) same (lane and substrate\$ and board)	2
<input type="checkbox"/>	L14	20040120418	2
<input type="checkbox"/>	L13	L12 and L10 and L3	0
<input type="checkbox"/>	L12	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes))	575
<input type="checkbox"/>	L11	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes)) and (disc\$ near3 drive)	8
<input type="checkbox"/>	L10	(375/211).ccls. or (455/7).ccls. or (455/14).ccls. or (455/15).ccls. or (455/16).ccls. or (370/279).ccls. or (370/293).ccls. or (379/338).ccls.	2895
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<input type="checkbox"/>	L8	L7 and L3	31
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<input type="checkbox"/>	L6	((synchroniz\$3 or clock\$ or lock\$3) near6 ((serializ\$3 near2 deserializ\$3) or serdes)) same (transceiver\$)) and (disc near3 drive)	7
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<input type="checkbox"/>	L4	L3 and L2	59
<input type="checkbox"/>	L3	@ad<=20040130	25461944
<input type="checkbox"/>	L2	((serializ\$3 near2 deserializ\$3) or serdes) same (disc near3 drive)	84
<input type="checkbox"/>	L1	((serializ\$3 near2 deserializ\$3) or serdes) same (phase near3 lack\$3) same (disc near3 drive)	0

END OF SEARCH HISTORY